Serial No. 10/553,665 Amdt. dated September 23, 2010 Reply to Office Action of June 23, 2010 PATENT PF030062 Customer No. 24498

## **Listing and Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) Data requesting device through at least one first communication network from at least one data server, wherein said data requesting device is able to support up to a maximum bandwidth rate, and said data requesting device comprising:

a sending module for sending requests of determined data to the server via at least one second communication network,

a receiving module for receiving streamed data from said server into said an input buffer via said first communication network and for providing said data to processing means for them to be exploited,

a retriever module for retrieving information comprising information representative of said maximum bandwidth rate, and

said sending module sends to said server via said second network said information, so that said server is able to determine at least one size of successive portions of said required data and at least one delay between two successive sending steps of said portions.

- 2. (Previously presented) Data requesting device according to claim 1, wherein said data requesting device comprises a data pump to extract data available in said input buffer and to transfer said data into a central memory for said data to be exploited by said processing means, said data pump being able to produce a pause control signal when said data in said central memory exceed a predetermined high threshold level of said central memory, and in that said sending module sends said pause control signal to said server.
- 3. (Previously presented) Data requesting device according to claim 1, wherein said data pump is able to produce a resume control signal when the data transfer from said input buffer to said central memory has been paused and said data in said central memory decrease down to a predetermined low

Serial No. 10/553.665

Amdt. dated September 23, 2010

Reply to Office Action of June 23, 2010

PF030062

PATENT

Customer No. 24498

threshold level of said central memory, and in that said sending module sends said resume control signal to said server.

4. (Previously presented) Data requesting device according to claim 2. wherein said data requesting device comprises an injector to transfer said data from said central memory to said processing means only when said data in said central memory fill up to a predetermined middle threshold level of said central memory.

5. (Previously presented) Data requesting device according to claim 2, wherein at least one of said threshold levels of said central memory depends on a round-trip time between said data requesting device and said server.

6. (Previously presented) Data requesting device according to claim 1, wherein said data requesting device is able to produce pause, resume and seek control signals for respectively pausing and resuming data streaming and for positioning at given appropriate places of said determined data, and said sending module sends to said server sequences of successively said pause, seek and resume control signals, so as to allow at least one feature among fast motion and reverse motion.

7. (Previously presented) Decoder, comprising a data requesting device according to claim 1.

8. (Previously presented) Data requesting process through at least one first communication network from at least one data server to a data requesting device wherein said data requesting device is able to support up to a maximum bandwidth rate and said data requesting process comprising the following steps:

sending requests of determined data to the server via at least one second communication network,

receiving streamed data from said server into said input buffer to then be exploited,

sending information comprising information representative of said maximum bandwidth rate from said data requesting device to said server via said second network, so that said server is able to determine at least one size of successive portions of said required data and at least one delay between two successive sending steps of said portions,

said data requesting process being executed by a data requesting device compliant with claim 1.

9. (Previously presented) Data transmitting device, wherein said data transmitting device comprises:

a receiving module for receiving requests of determined data from at least one data requesting device,

specification means for determining at least one size of successive portions of said data to be provided to said data requesting device, and

a streaming module for triggering streaming of said data portions to said data requesting device.

said receiving module receiving from said data requesting device information representative of capacities of said data requesting device, and said specification means determining said portion size in function of said information,

said capacities comprising a maximum bandwidth rate being supported by said data requesting device;

said specification means determining at least one delay between two successive sending steps of said portions in function of said information,

and said streaming module periodically triggers streaming of said data portions having said portion size to said data requesting device, with a period equal to said delay,

said data transmitting device being provided for a data requesting device according to claim 1.

10. (Previously presented) Data transmitting device according to claim 9, wherein said receiving module receives pause control messages from said Serial No. 10/553,665 Amdt. dated September 23, 2010

Reply to Office Action of June 23, 2010

PATENT PF030062 Customer No. 24498

data requesting device, and said streaming module pauses said data

streaming when one of said pause control messages is received.

11. (Previously presented) Data transmitting device according to claim

9, wherein said data requesting device is able to support up to a maximum

bandwidth rate, said capacities comprise said maximum bandwidth rate.

12. (Previously presented) Data transmitting device according to claim

9, wherein said receiving module receives slow motion messages from said

data requesting device, and said specification means determines at least one

increased value of said period when said slow motion messages are received.

13. (Previously presented) Data transmitting device according to claim

9, wherein said receiver receives at least one kind of messages among fast

motion and reverse motion messages, from said data requesting device, and

said data transmitting device comprises a parsing module able to identify

successive relevant places in said determined data for at least one of said fast

and reverse motions, said specification means successively positioning at

said places, when one of said fast motion and reverse motion messages is

received.

14. (Previously presented) Data transmitting process, wherein said

data transmitting process comprises the following steps:

receiving requests of determined data from at least one data

requesting device, as well as information representative of capacities of said

data requesting device,

determining at least one size of successive portions of said data to be

provided to said data requesting device, in function of said information, and

streaming said data portions to said data requesting device,

said capacities comprise a maximum bandwidth rate of said data

requesting device;

5

Serial No. 10/553,665 Amdt. dated September 23, 2010 Reply to Office Action of June 23, 2010 PATENT PF030062 Customer No. 24498

said specification step comprises determination of at least one delay between two successive sending steps of said portions in function of said information, and

said streaming step includes periodically streaming said data portions having said portion size to said data requesting device, with a period equal to said delay,

said data transmitting process being executed by a data transmitting device compliant with claim 9.

15. (Previously presented) Computer program product, comprising program code instructions for executing the steps of the process of claim 8 when said program is executed on a computer.